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**EXPEDITED PROCEDURE**  
**REPLY UNDER 37 C.F.R. § 1.116**  
**ART UNIT 2183**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/700,391  
Filed: November 4, 2003  
Inventor(s):  
Arun Radhakrishnan and Kelvin  
D. Goveas

Title: Prediction of Floating Point  
Instruction Latency and  
Scheduling Based on  
Prediction

Examiner: Cody, Dillon J.  
Group/Art Unit: 2183  
Atty. Dkt. No: 5500-91300

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Lawrence J. Merkel

Printed Name

Signature

Date

5/25/06

**RESPONSE TO OFFICE ACTION OF**  
**April 24, 2006**

Mailstop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of April 24, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

Do Not Enter  
6-22-06